

(b) forming a metal or a metal compound for providing on an inside of said holes, and then forming cylindrical first electrodes by forming a metal film or a metal compound film covering the inner wall of said holes;

(c) depositing a dielectric capacitance insulating film to cover said first electrodes, and depositing further a ruthenium film and a conductor layer by a sputtering method;

(d) patterning said ruthenium film and conductor layers to form second electrodes; and

(e) depositing a third inter-layer insulating film covering said second electrodes, and forming a first connection hole reaching said second electrode and a second connection hole reaching said first layer wiring, by etching,

wherein said conductor layer comprises a tungsten film and said third inter-layer insulating film comprises a silicon oxide film.

21. (Twice Amended) A method of producing a semiconductor integrated circuit device according to claim 20, wherein, after said second conductive layer is etched, said ruthenium film is etched by using said conductive layer, that is patterned, as a mask.

22. (Twice Amended) A method of producing a semiconductor integrated circuit device including the steps of:

(a) forming first electrodes on a first insulating film formed on a main plane of a semiconductor substrate;

(b) forming a capacitance insulating film over said first electrode;

(c) forming second electrodes over said capacitance insulating film;

(d) forming a second insulating film on said second electrodes;

(e) forming an opening for exposing a part of said second electrodes into said second insulating film by using photoresist film as a mask;

(f) ashing said photoresist film; and

(g) forming a conductor layer inside said opening;

wherein the formation step of said second electrode includes the steps of:

(i) forming a ruthenium film by a chemical vapor phase growing method containing oxygen over said capacitance insulating film; and

(ii) forming a metal layer not containing oxygen over said ruthenium film,

wherein said ruthenium film directly contacts to said metal layer.

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24. (Amended) A method of producing a semiconductor integrated circuit device according to claim 22, wherein said metal layer comprises a tungsten film or a tungsten nitride film.

25. (Amended) A method of producing a semiconductor integrated circuit device according to claim 22, wherein said metal layer is formed by a sputtering method.

26. (Twice Amended) A method of producing a semiconductor integrated circuit device including the steps of:

(a) forming a plurality of mutually spaced-apart first electrodes over a first insulating film formed on a main plane of a semiconductor substrate;

(b) forming a capacitance insulating film over said first electrodes;

(c) forming continuous second electrodes with respect to a plurality of said

first electrodes, over said capacitance insulating film;

(d) forming a second insulating film in order to cover said second electrodes;

(e) forming a hole for exposing a part of said second electrodes into said second insulating film by using photoresist film as a mask;

(f) ashing said photoresist film; and

(g) forming a conductor layer inside said hole;

wherein the formation step of said second electrodes includes the steps of:

(i) forming a ruthenium film over said capacitance insulating film; and

(ii) forming a metal layer having a greater film thickness than said ruthenium film over said ruthenium film, and said metal layer has a lower resistivity than said ruthenium film,

wherein said metal layer has higher oxidation resistance than said ruthenium film.

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28. (Amended) A method of producing a semiconductor integrated circuit device according to claim 26, wherein said metal layer is a tungsten film or a tungsten nitride film.

29. (Twice Amended) A method of producing a semiconductor integrated circuit device including the steps of:

(a) forming a plurality of mutually spaced-apart first electrodes over a first insulating film formed on a main plane of a semiconductor substrate;

(b) forming a capacitance insulating film over said first electrodes; and

(c) forming a continuous second electrode with respect to a plurality of said first electrodes, over said capacitance insulating film; wherein:

the formation step of said second electrode includes the steps of:

(i) forming a ruthenium film over said capacitance insulating film in such a fashion as to provide within the spaces between said mutually spaced-apart first electrodes by using a CVD method; and

(ii) forming said metal layer over said first metal layer;

wherein said metal layer has higher oxidation resistance than said ruthenium film.

30. (Amended) A method of producing a semiconductor integrated circuit device according to claim 29, wherein said metal layer is formed by a sputtering method.

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35 31. (Amended) A method of producing a semiconductor integrated circuit device according to claim 29, wherein said metal layer comprises a first metal layer formed by a sputtering method and a second metal layer formed by a chemical vapor phase growing method over said first metal layer.

32. (Amended) A method of producing a semiconductor integrated circuit device according to claim 29, wherein the film thickness of said metal layer is greater than that of said ruthenium film.

33. (Amended) A method of producing a semiconductor integrated circuit device according to claim 29, wherein said metal layer is a tungsten film or a tungsten nitride film.

34. (Amended) A method of producing a semiconductor integrated circuit device according to claim 29, wherein said ruthenium film comprises a first ruthenium film formed by a sputtering method and a second ruthenium film formed by a chemical vapor phase growing method over said first ruthenium film.

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